Jahanvi Agrawal

ECE sophomore at IIIT-Bangalore

Highly motivated & passionate female engineering student wanting to make meaningful career in the field of Electronic and Communication engineering and Information Technology.

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 - 1234-jahanvi.github.io/ 🥽
- linkedin.com/in/jahanvi-agrawal-a861171ab in
 - github.com/1234-jahanvi 💭
 - instagram.com/jahanvi_32 👩

EDUCATION

iMTech - Electronics and Communication Engineering

International Institute of Information Technology, Bangalore

CGPA: 3.45/4.0

Score

08/2019 - Present

- First semester: 3.44/4.0
- Second Semester: 3.29/4.0
- Third Semester: 3.63/4.0

Class 11th and 12th State Board

Pace Junior Science College

2017 - 2019 Score

Mumbai. Maharashtra

- Secured 89.69% in 12th Std Maharashtra 2019 State Board exams

Class 9th and 10th ICSE Board

Lilavatibai Podar Senior Secondary School

2015 - 2017 Score

Mumbai, Maharashtra

- Secured 94.44% in the 10th Std ICSE 2017 Board exams (15th Rank in the school)

WORK EXPERIENCE

Speaker Curator

TEDxIIITBangalore 01/2020 - Present

Bangalore, Karnataka

Developer

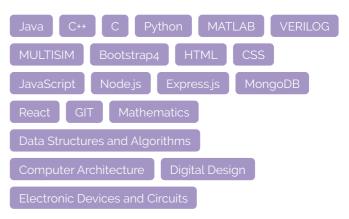
ZENSE - the official Software Development club of IIIT-Bangalore 11/2019 - Present

Bangalore, Karnataka

Volunteering at I-STEM Hackathon 2020 **IIIT-Bangalore**

A 24-hour hackathon where blind and visually impaired developers, professionals, and engineering students work on exciting and challenging problems

SKILLS



PROJECTS

WATCH-OUT

- Tech Stack: HTML, CSS, JavaScript
- A simple version of the game, Flappy Bird and built this as part of ZENSE HACKNIGHT 2019 (an overnight hackathon) organized by ZENSE
- https://github.com/zense/Canvas-

POMODORO CLOCK

- Tech Stack: Advanced JavaScript, HTML, CSS
- A web application to help students study in time-frames with planned breaks.
- ZENSE recruitment project
- <u>https://github.com/1234-jahanvi/POMODORO-CLOCK</u>

ARDUINO-PHONE

- The main aim of this ARDUINO-based project is to conveniently make a call, answer a call, send SMS, and read SMS.
- https://drive.google.com/file/d/1D10Rm39VXDmkWqDG5c6ADxL cFMYIT4qK/view

IAS-COMPUTER IMPLEMENTATION

- Implemented the IAS (Princeton) computer entirely in VERILOG with an ISA of 12 instructions as part of Computer Architecture course
- https://github.com/1234-jahanvi/Verilog-Programming/tree/main/IAS%20implementation

CACHES IMPLEMENTATION

- Implemented two types of Cache: Direct-Mapped and 4-way Set Associative Cache in VERILOG
- https://github.com/1234-jahanvi/Verilog-Programming/tree/main/Caches

MULTIPLAYER QUIZ GAME SHOW

- Tech Stack: Socket Programming in Python3
- https://github.com/1234-jahanvi/Socket-Programming